

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2002-111637

(43)Date of publication of application : 12.04.2002

(51)Int.Cl.

H04L 1/00 H04B 7/26

H04L 1/18 H04L 27/00

(21)Application number : 2000-293534

(71)Applicant : HITACHI KOKUSAI ELECTRIC INC

(22)Date of filing : 27.09.2000

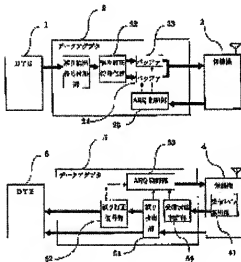
(72)Inventor : NAKABAYASHI SUMIE

(54) RETRANSMISSION CORRECTION METHOD

(57)Abstract:

PROBLEM TO BE SOLVED: To provide an error retransmission correction method in which a data transmission of satisfactory efficiency can be realized, in such a way that an error correction bit at a high coding rate can be controlled so as to be transmitted even when a bit error rate is high or even when the bit error rate is low.

SOLUTION: A means by which an error correction is coded at a plurality of coding rates is installed on the transmission side of data. A means by which the bit error rate is estimated on the basis of reception level information is installed on the reception side of the data. When a retransmission is requested, the retransmission request is performed in such a way that bit error rate information on the reception side is contained in a retransmission request signal. On the transmission side of the



data, an error correction code bit at a coding rate corresponding to the bit error rate information contained in the retransmission request signal is transmitted as a retransmission signal.